#### INTERNATIONAL SEARCH REPORT

/IB2004/002641

A. CLASSIFICATION OF SUBJECT MATTER IPC 7 G09G3/36

According to International Patent Classification (IPC) or to both national classification and IPC

#### B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)  $IPC\ 7\ G09G$ 

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

#### EPO-Internal

		<del></del>
Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
х	CRONE B ET AL: "LARGE-SCALE COMPLEMENTARY INTERGRATED CIRCUITS BASED ON ORGANIC TRANSISTORS" NATURE, MACMILLAN JOURNALS LTD. LONDON, GB,	1,2
	vol. 403, 3 February 2000 (2000-02-03), pages 521-523, XP000929929 ISSN: 0028-0836	•
Y	page 521, right-hand column, paragraph 1 page 522, left-hand column, line 17 - line 30	3–12
	page 522, left-hand column, line 45 - page 522, right-hand column, line 42; figures 2-4	*
	-/	
		}

Further documents are listed in the continuation of box C.	X Patent family members are listed in annex.
<ul> <li>Special categories of cited documents:</li> <li>"A" document defining the general state of the art which is not considered to be of particular relevance</li> <li>"E" earlier document but published on or after the international filing date</li> <li>"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)</li> <li>"O" document referring to an oral disclosure, use, exhibition or other means</li> <li>"P" document published prior to the international filing date but later than the priority date claimed</li> </ul>	<ul> <li>"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention</li> <li>"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone</li> <li>"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.</li> <li>"&amp;" document member of the same patent family</li> </ul>
Date of the actual completion of the international search  24 November 2004  Name and mailing address of the ISA  European Patent Office, P.B. 5818 Patentlaan 2  NL - 2280 HV Rijswijk  Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,  Fax: (+31-70) 340-3016	Date of mailing of the International search report  03/12/2004  Authorized officer  Morris, D

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/IB2004/002641 C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	TOUWSLAGER F J ET AL: "I-Line lithography of poly-(3,4-ethylenedioxythiophene) electrodes and application in all-polymer integrated circuits"  APPLIED PHYSICS LETTERS, AMERICAN INSTITUTE OF PHYSICS. NEW YORK, US, vol. 81, no. 24, 9 December 2002 (2002-12-09), pages 4556-4558, XP012032748 ISSN: 0003-6951	1,2
Y	page 4556, left-hand column, line 1 page 4557, left-hand column, paragraph 3 - page 4558, right-hand column, paragraph 1; figures 2,4	3-12
Α	US 4 948 232 A (LANGE ALF) 14 August 1990 (1990-08-14) column 2, line 9 - column 2, line 24 column 3, line 1 - column 3, line 24; figures 1,2,4 column 3, line 44 - column 4, line 3	1,4-12
<b>A</b>	US 5 019 807 A (STAPLETON BARBARA K ET AL) 28 May 1991 (1991-05-28) column 1, line 32 - column 2, line 3 column 2, line 28 - column 2, line 59; figure 1 column 5, line 19 - column 6, line 39; figures 5,6	1,4-12
Y	US 2002/149318 A1 (JEON JIN ET AL) 17 October 2002 (2002-10-17) paragraph '0094! - paragraph '0107!; figure 4 paragraph '0113! - paragraph '0141!; figures 6-11	3–12
A	GELINCK G H ET AL: "High-performance all-polymer integrated circuits" APPLIED PHYSICS LETTERS, AMERICAN INSTITUTE OF PHYSICS. NEW YORK, US, vol. 77, no. 10, 4 September 2000 (2000-09-04), pages 1487-1489, XP012026061 ISSN: 0003-6951 cited in the application page 1489, left-hand column, line 7 - page 1489, right-hand column, line 30  -/	1-12

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A	KLAUK H ET AL: "Polymer gate dielectric pentacene TFTs and circuits on flexible substrates" INTERNATIONAL ELECTRON DEVICES MEETING 2002. IEDM. TECHNICAL DIGEST. SAN FRANCISCO, CA, DEC. 8 - 11, 2002, NEW YORK, NY: IEEE, US, 8 December 2002 (2002-12-08), pages 557-560, XP010626105 ISBN: 0-7803-7462-2 page 557, left-hand column, paragraph 1 page 559, right-hand column, paragraph 2 page 559, right-hand column, paragraph 2; figure 3	1-12
A	MACH P ET AL: "Monolithically integrated, flexible display of polymer-dispersed liquid crystal driven by rubber-stamped organic thin-film transistors"  APPLIED PHYSICS LETTERS, AMERICAN INSTITUTE OF PHYSICS. NEW YORK, US, vol. 78, no. 23, 4 June 2001 (2001-06-04), pages 3592-3594, XP012028198 ISSN: 0003-6951 cited in the application the whole document	1-12

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